

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: JARABEK, Andrew, Louis; TOMBUL, Aris; LAU, Warren, H.
Serial No.: 10/675,097
Filed: September 30, 2003
Title: MEMORY INTEGRITY SELF CHECKING IN VT/TU CROSS-
CONNECT
Group: 2138
Examiner:
Attorney Ref.: PAT 2509-2 US

October 9, 2007

Commissioner for Patents
United States Patent and Trademark Office
P.O. Box 1450
Alexandria, Virginia
22314-1450, U.S.A.

Attention: Certificate of Corrections Branch

Dear Sir:

REQUEST FOR CERTIFICATE OF CORRECTION

On behalf of the patentee, we hereby request that a Certificate of Correction be issued pursuant to 37 C.F.R. 1.322, to correct the error made by the Office as shown in the attached in form PTO/SB/44 submitted herewith.

The errors were introduced by the Office. Accordingly, no fee is payable by the Applicant pursuant to 35 U.S.C. 254.

Respectfully submitted,
Andrew Louis JARABEK et al.

By: /Shin Hung/

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

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PATENT NO. : 7,246,289

APPLICATION NO.: 10/675,097

ISSUE DATE : July 17, 2007

INVENTOR(S) : Andrew Louis JARABEK et al.

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 5, claim 11, line 44, delete "embodies" and insert therefor -- embodied --;

Column 6, claim 17, line 21, delete "switching environment" and insert therefor -- network switching environment --;

Column 6, claim 17, line 23, delete "of" and insert therefor -- of the --; and

Column 6, claim 17, line 25, delete "generate and store" and insert therefor -- generating and storing --

MAILING ADDRESS OF SENDER (Please do not use customer number below):

Borden Ladner Gervais LLP
1100-100 Queen Street, Ottawa, Ontario, KP 1J9, CANADA

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